REMARKS

In accordance with the foregoing, claims 1-40 are pending and under consideration. The specification has been amended to correct an incomplete sentence and to clarify another sentence. Claims 6 and 35 have been amended to correct grammatical errors. Claims 1-40 stand rejected. The Examiner's rejections are traversed below. No new matter is included in this amendment.

The Rejection:

At page 2 of the Office Action, claims 1-40 are rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent 5,570,340 to Lee et al. This rejection is respectfully traversed. Anticipation requires a disclosure in a single prior art reference of each feature recited in the claim, arranged as in the claim.

Claim 1 recites a data structure comprising "first data and second data which are different from each other in type and formed of a respective plurality of data units." In contrast, as disclosed at col. 7, lines 15-48, Lee et al. disclose that a plurality of first programs and second programs are respectively recorded according to index number in the first data area (18) and in the second area (20) having start addresses (D and E), respectively.

Further, claim 1 recites "a first data unit descriptor table and a second data unit descriptor table in which information on the first data units and the second data units is respectively recorded as data unit descriptors." The Examiner asserts that Figure 3 of Lee et al. shows the "first table descriptor" 14 and the "second table descriptor" 18 as further described at column 6, lines 44-52. Col. 6, lines 44-52 refer to a "first table area" 14, a "second table area" 16, a "first data area" 18 and "a second data area" 20. No mention is made of a "first table descriptor" 14 and a "second table descriptor" 18. The first table area 14 and the second table area 16 of Lee et al. consist of respective tables having a number, a start address, and characteristics data of each first program of first data area (18) and each second program of second data area (20), respectively.

As shown in Figs. 5 and 6 of Lee et al., the first and second table areas 14 and 16 have a same structure. An "order table" 24c included within the second data area 20 is not a part of either the first table area 14 or the second table area16. Thus, Lee et al. do not disclose the feature that "the information on the first data units, which are to be reproduced after the first data units are linked to the second data units, is included in the program descriptor of the second

program descriptor table," as recited in claim 1.

Claims 2-17, 23-30 and 38-40 are deemed to be patentable at least for similar reasons set forth above regarding claim 1.

Regarding claim 18, the Examiner asserts that Lee et al. disclose a recording apparatus for recording data of different types on a recording medium. However, no mention is made in Lee et al of an apparatus for recording data of different types. The digital signal processor 150 described at col. 8, lines 15-20, to which the Examiner refers, is for reproduction only and fails to disclose "a first signal processor which formats a first data stream input into independent units, codes the formatted first data units, and provides the coded first data units to the recording medium, a second signal processor which formats a second data stream input into independent units, codes the formatted second data units, and provides the coded second data units to the recording medium; and a system controller which generates a first data unit descriptor table and a second data unit descriptor table, in which information on the first data units and the second data units are respectively stored as data unit descriptors, and a first program descriptor table and a second program descriptor table, in which information on one or more data units forming respective programs are stored in a program descriptor of a corresponding program in the form of information on respective data unit descriptors, wherein information on the first data unit, which is to be reproduced after being linked to the second data unit, is included in the program descriptor of the second program descriptor table, as recited in claim 18.

Claims 19-22 are deemed to be patentable at least for similar reasons set forth above regarding claim 18.

Regarding claim 31, Lee et al. fail to disclose a combination recording/reproducing apparatus as recited in claim 31. Claim 31 is deemed to be patentable at least for similar reasons set forth above regarding claim 1 and for the following reason. By way of example, Lee et al. fail to disclose "a first signal processor which formats first data input into independent units and codes the formatted first data units during recording," "a second signal processor which formats second data input into independent units, codes the formatted second data units during recording," and "a system controller which during recording, generates a first data unit descriptor table and a second data unit descriptor table storing information on the first data units and the second data units as data unit descriptors, and generates a first program descriptor table and a second program descriptor table in which information on one or more data units forming respective programs is stored in the program descriptor of the corresponding program in the

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form of information on respective data unit descriptors, wherein the information on the first data unit to be reproduced after being linked to the second data unit is included in the program descriptor of the second program descriptor table," as recited in recited in claim 31.

Claims 32-37 are deemed to be patentable at least for similar reasons set forth above regarding claim 31.

Conclusion:

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.
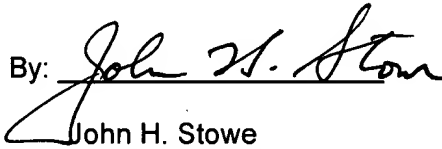
Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 503333.

Respectfully submitted,

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